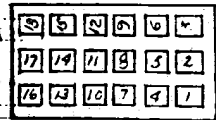
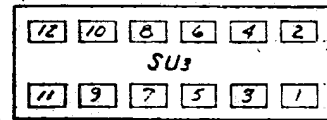


NUMERATOR ACC. INTERCONNECTOR TERMINAL

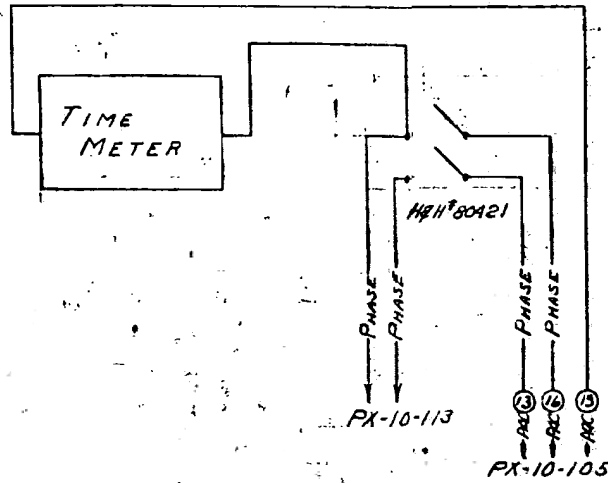


SV

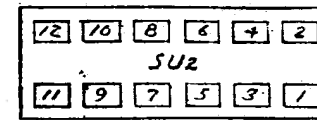
DENOMINATOR - SQUARE-ROOT ACC. PROGRAM TERMINAL



SU3

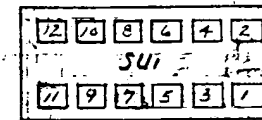


QUOTIENT ACC. & SHIFT ACC. PROGRAM TERMINAL



SU2

DIGIT ANSWER OUTPUT TERMINAL



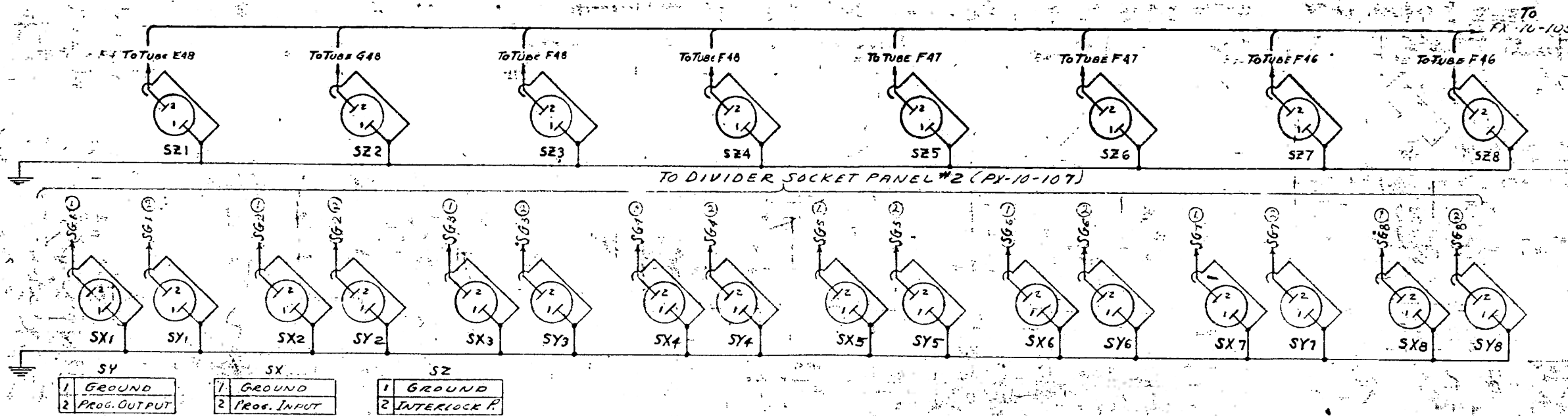
SU1

SV	
1	NA
2	NA
3	NA
4	/
5	/
6	NA
7	/
8	/
9	NC
10	/
11	/
12	/

SU3	
1	Sx
2	DA
3	DY
4	DA
5	DS
6	/
7	/
8	DISPOSAL 3
9	CLEAR 3
10	DISPOSAL 4
11	CLEAR 4
12	GROUND

SU2	
1	QA
2	Sx
3	SA
4	/
5	/
6	/
7	Sc
8	DISPOSAL 1
9	CLEAR 1
10	DISPOSAL 2
11	CLEAR 2
12	GROUND

SU1	
1	UNITS OUTPUT
2	TENS OUTPUT
3	HUNDREDS OUTPUT
4	THOUSANDS OUTPUT
5	TEN THOUSANDS OUTPUT
6	HUNDREDS THOUSANDS OUTPUT
7	MILLIONS OUTPUT
8	TEN MILLIONS OUTPUT
9	HUNDREDS MILLIONS OUTPUT
10	BILLIONS OUTPUT
11	PA1
12	GROUND



**REVISIONS**

1. *SU1 & SU3 REWIRING* 8/29/46

2. *TIME METER & SWITCH REVERSED. GROUNDS ADDED.*

3. *SZ MOVED FROM POSITION OVER SX TO OVER SY.*

4. *TERMINAL 1 ON ALL SZ02 PLUGS GROUNDED.*

5. *TIME METER WIRING REVISED FOR WYE CONNECTION OF A.C. POWER.*

6. *...*

MOORE SCHOOL OF ELECTRICAL ENGINEERING  
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**DIVIDER FRONT PANEL**

MATERIAL: \_\_\_\_\_ FINISH: \_\_\_\_\_ SCALE: \_\_\_\_\_

Drawn by: *elp* 3/23/45

Checked by: *...*

Approved by: *...*

PX-10-108