O.O. FORM 8 AUG 49 ORDIP 1177-3

TIMING CHART FOR THE PROGRAMMING OF THE AUXILIARY MEMORY EQUIPMENT AND ENIAC FOR STORE AND EXTRACT ORDERS

17 18 0 2 4 6 8 10 12 14 16 18 0 2 4 6 8 10 12 14 16 18 0 2 4 6 8 10 12 14 16 18 0

DATE INPUT

97 H-5 — TRANSC. 30 SET
RECEIVE ADDRESS FROM ACC # 8 | ADDRESS | AVAILABLE
SET TRANSC. 27 INITIATE EXTRACT
TRANSC. 27 RESET CLEARS ADDRESS
ADD MEMORY OUT TO ACC # 15

98 H-5 — TRANSC. 30 SET
USE DASHED LINE ONLY IF ORDER IS CONTAINED IN MEMORY
RECEIVE ADDRESS FROM ACC # 6 | ADDRESS | AVAILABLE
SET TRANSC. 27 INITIATE EXTRACT
TRANSC. 27 RESET CLEARS ADDRESS
SET TRANSC. 28 & PM
ADD MEMORY OUT TO ORDER SELECTOR
RECEIVE ADDRESS FROM C-7, C-8 THROUGH PM | ADDRESS | AVAILABLE
TRANSC. 28 RESET INITIATE EXTRACT
RESET PM (C-5)
ADD MEMORY OUT TO ACC # 15

95 B-8 — TRANSC. 29 SET
RECEIVE ADDRESS FROM ACC # 8 | ADDRESS | AVAILABLE
SET GATED PM INITIATE STORE
RECEIVE FROM ACC # 15
FT 4 TRANSC. 11 CLEARS ADDRESS

96 H-5 — TRANSC. 30 SET
USE DASHED LINE ONLY IF ORDER IS CONTAINED IN MEMORY
RECEIVE ADDRESS FROM ACC # 6 | ADDRESS | AVAILABLE TO MEMORY
SET TRANSC. 27 INITIATE EXTRACT
TRANSC. 27 RESET CLEARS ADDRESS
SET PM
ADD MEMORY OUT TO ORDER SELECTOR
RECEIVE ADDRESS FROM C-7, C-8 THROUGH PM | ADDRESS | AVAILABLE
RESET INITIATE PM STORE
RECEIVE WORD TO BE STORED FROM ACC # 15
FT 4 TRANSC. 11 CLEARS ADDRESS

DATE OUTPUT

MAGNETIC STATIC MEMORY

PROGRAM STORE
INPUTS EXTRACT

ACC # 15
OUTPUT
001 | ACI
INPUT
R-5 | E-4

ACC # 6
STORE ADDRESS OF ORDERS

ACC # 8
STORE ADDRESS OF 95 & 97 ORDERS

ACC # 14
002 | 001

ACC # 11
001

B-11 | K-7 | B-11 | M-9 | B-11

CONVERTER OUTPUT FOR 96 STORE

TRAY II 11 CHANNELS

GATED PULSE AMPLIFIER WITH SPECIAL TRANSFORMERS SEE PX-4-309

SHORT TRAY II 11 CHANNELS

PX-4-124
ORDER SELECTOR 5 4 3 2 1 0
C-7
C-8

11 CHANNELS

TRAY IV 11 CHANNELS

STANDARD PULSE AMPLIFIER TO DRIVE ENIAC TRANSMISSION TRAYS SEE PX-4-301

SHORT TRAY IV 11 CHANNELS

C-8
C-7
PM TO GATE C-7, C-8 TO di RING INPUTS PX-4-121
M-9 | C-5

ISOLATION PA
11 CHANNELS
1 2 3 4 5 6 7 8 9 10 11
E-4 | F-1 | K-7 (STORE) | B-11 | E-6

STANDARD PA PX-4-301

FROM TRAY IV
2 PINS
1
INPUTS

FROM PM
di INPUTS

RING CLEAR

DRIVERS
0 1 2 3 4 5 6 7 8 9
TENS
UNITS
DRIVERS

10 D.C. LINES TO "B" ADDRESS SELECTORS

10 D.C. LINES TO "G" ADDRESS SELECTORS

LOCATED IN CONSTANT TRANSMITTER See PX-11-310

CONSTANT TRANSMITTER TRANSC. # 30 EXTRACT FOR 97 ORDER
001 GRP
H-5

TRANSC # 29 STORE FOR 95 ORDER
001 CPP
B-8

TRANSC. # 28 EXTRACT FOR 98 ORDER
001 GRP
K-6

TRANSC. # 27 CLEAR RING AT TIME 19 AFTER EXTRACT ORDER
001 RP

FUNCTION TABLE IV TRANSC # 11
001 CPP
K-7

MINN. C.A. 4-67 CIV 138
PLAINTIFF'S TRIAL EXHIBIT
Nº 8425
015263

SYM	DESCRIPTION	DATE	APPROVAL

REVISIONS

PHYSICAL PROPERTIES	
YP	
TS	
EL 2	
RA	
BH	
RH	

NEXT ASSY | USED ON
APPLICATION
DO NOT | APPLY PART NO.
DO * | AS SPECIFIED

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES
TOLERANCES ON DECIMALS, ANGLES, FRACTIONS
MATERIAL
HEAT TREATMENT
FINAL PROTECTIVE FINISH

ORIGINAL DATE OF DRAWING 11/12/53
DRAFTSMAN ALD | CHECKER
TRACER | CHECKER
ENGINEER | ENGINEER
SUBMITTED ORD CORPS
APPROVED BY ORDER OF THE CHIEF OF ORDNANCE ORD CORPS

INTERCONNECTION DIAGRAM FOR THE MAGNETIC STATIC MEMORY AND THE ENIAC

SCALE | UNIT WT

ORDNANCE CORPS
DEPT OF THE ARM

DWG SIZE C | PX-11-311
SHEET OF