

# MASTER PROGRAMMER PANEL 1

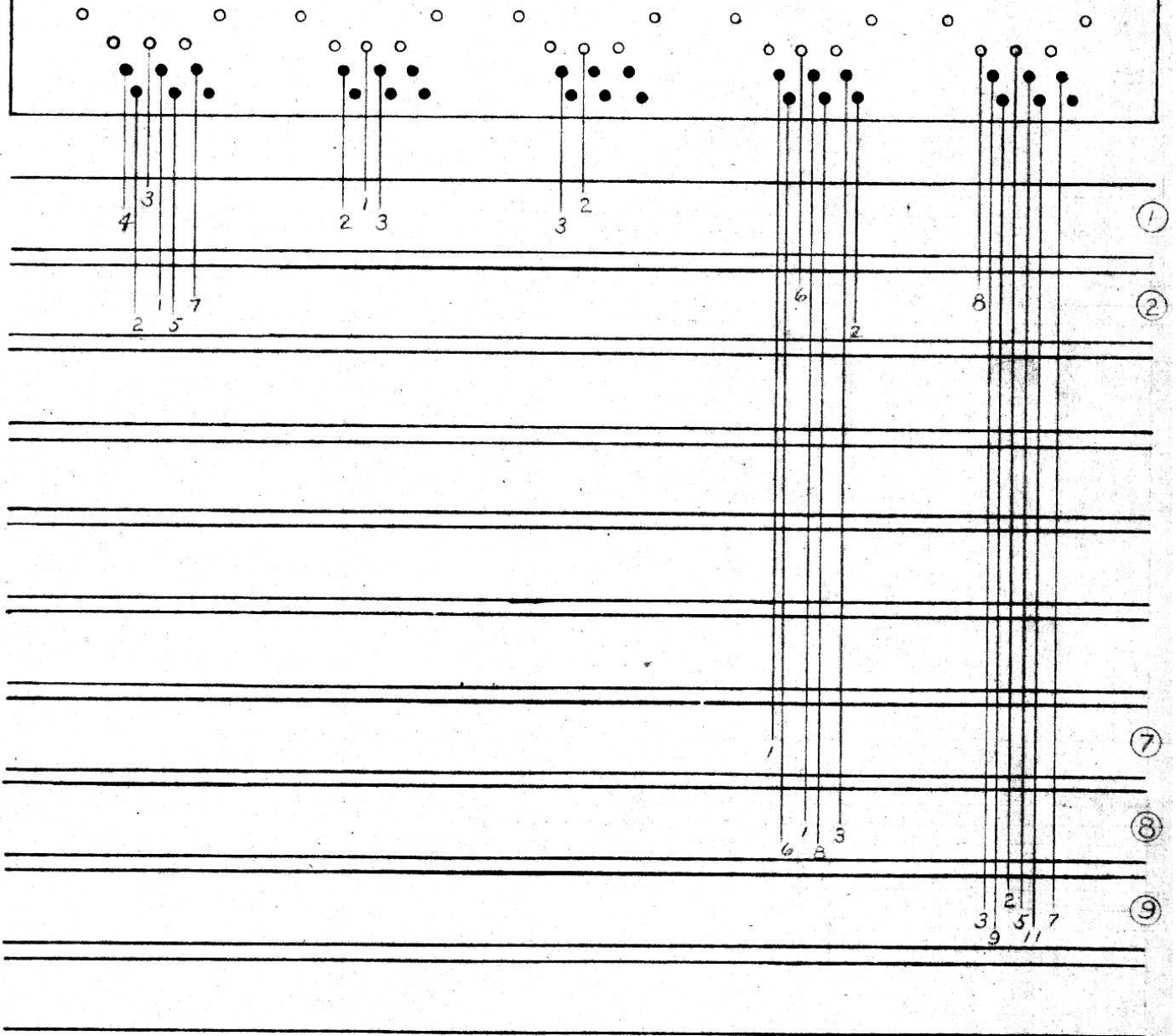
A	B	C	C	C	C	D	E
1	1		1	0	0		1
1	1				1		1
1							1

1							1
1							1

A 5	B 2	C 2	D 6	E 5
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PX-11-405 (b)

Fig. 8-3 (b)

SET-UP DIAGRAM FOR SEQUENCE 1 AND SEQUENCE 2.1

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