

MASTER PROGRAMMER
PANEL 2

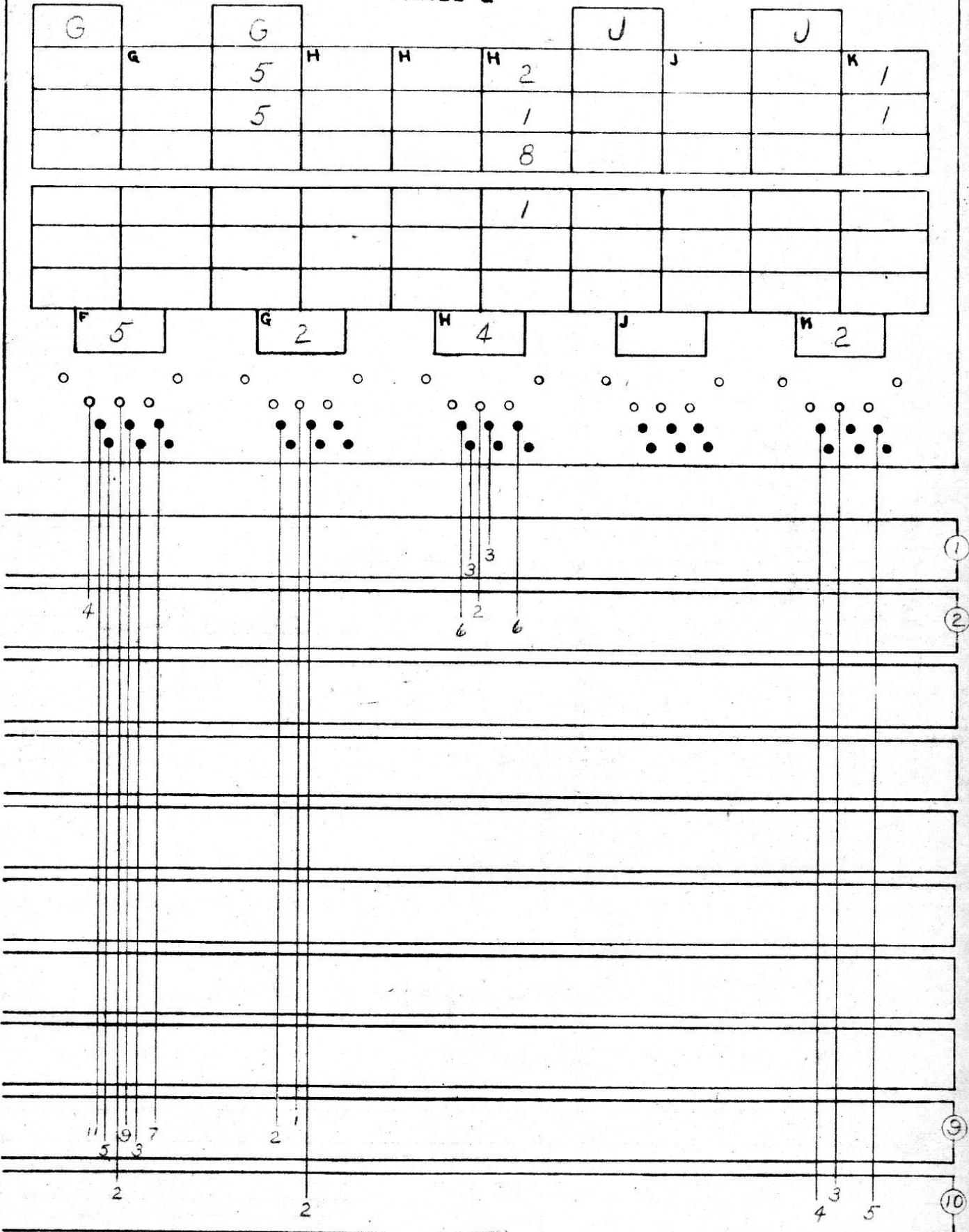


Fig. 8-3 (c)

SET-UP DIAGRAM FOR SEQUENCE 1 AND SEQUENCE 2.1

PX-11-403(c)

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