



SL-OUTPUT FROM 4, 5
 SR-OUTPUT FROM 2, 5
 SL'-OUTPUT FROM 2, 3, 5
 SR'-OUTPUT FROM 1, 4, 5

NOTE: PLUG IN RESISTORS
 IN PROGRAM TRAM FROM
 CONVERTERS

REV.	DESCRIPTION	DATE	INITIALS

PROGRAM MATRIX BOX

ORDNANCE CORPS
 DEPT OF THE ARMY

DES. NO. PX-17-102
 REV. B

UNIT INT. SCALE

ORIGINAL DATE: 10/15/53

CE PROGRAMING PROGRAM A/D

TRACER: BRECKER, BRECKER

SUBMITTED: BRECKER

APPROVED BY: CHIEF OF THE BRANCH

CHIEF OF BRANCH

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES

TOLERANCES ON DECIMALS

FRACTIONS

HEAT TREATMENT

DO NOT APPLY PART NO. FINAL PROTECTIVE FINISH

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