

BLANK

2

3

4

5

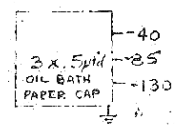
6

7

8

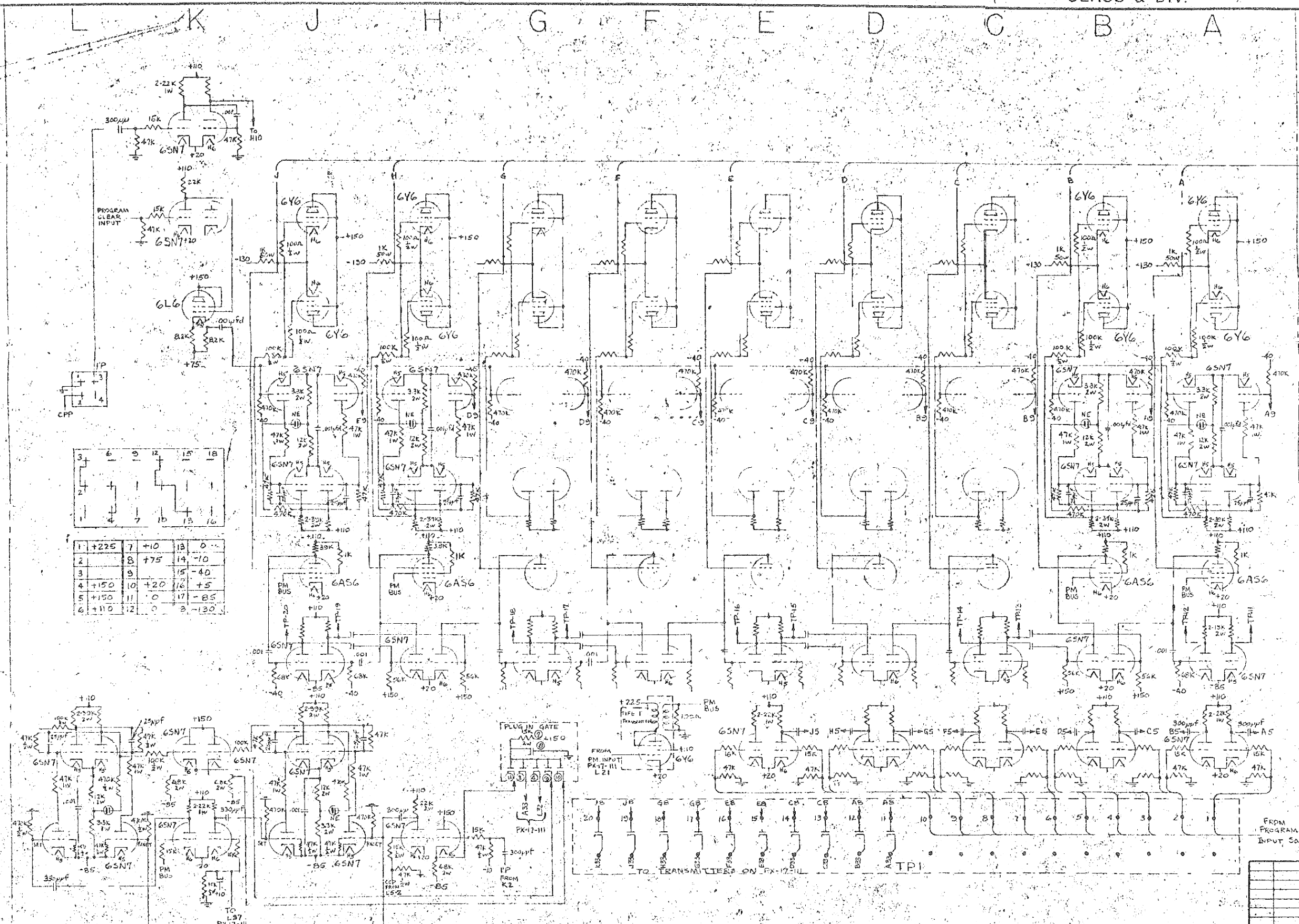
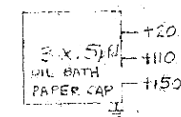
9

10



1	-130
2	-85
3	-40
4	-10
5	0
6	+10
7	+20
8	+5
9	+75
10	+110
11	+150

JONES TB #1 PIN



1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
1	+	225	7	+	10	13	0								
2			8	+	75	14	-	70							
3			9			15	-	40							
4	+	150	10	+	20	16	+	5							
5	+	150	11	0	17	-	85								
6	+	110	12	0	18	-	130								

NOTE: ALL NEONS ARE LOCATED ON FRONT PANEL

SYM	DESCRIPTION	DATE	APPROVAL

PHYSICAL PROPERTIES	UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES	ORIGINAL DATE OF DRAWING	10/27/53
VP	TOLERANCES ON DECIMALS	DESIGNED BY	CHICKER H.C.
TS	ANGLES FRACTIONS	TRACER	
EL 2	MATERIAL	ENGINEER	
RA	HEAT TREATMENT	ENGINEER	N.C.
BH	APPLICATION	SUBMITTED	
BH	DO NOT APPLY PART NO. AS SPECIFIED	APPROVED BY ORDER OF THE CHIEF OF ORDNANCE	

PROGRAM FLIP FLOPS,
GATE MATRIX DRIVERS
AND PM CORRECTION
CIRCUITS.

ORDNANCE CORPS
DEPT OF THE ARMY

SCALE UNIT WT

PX-17-110