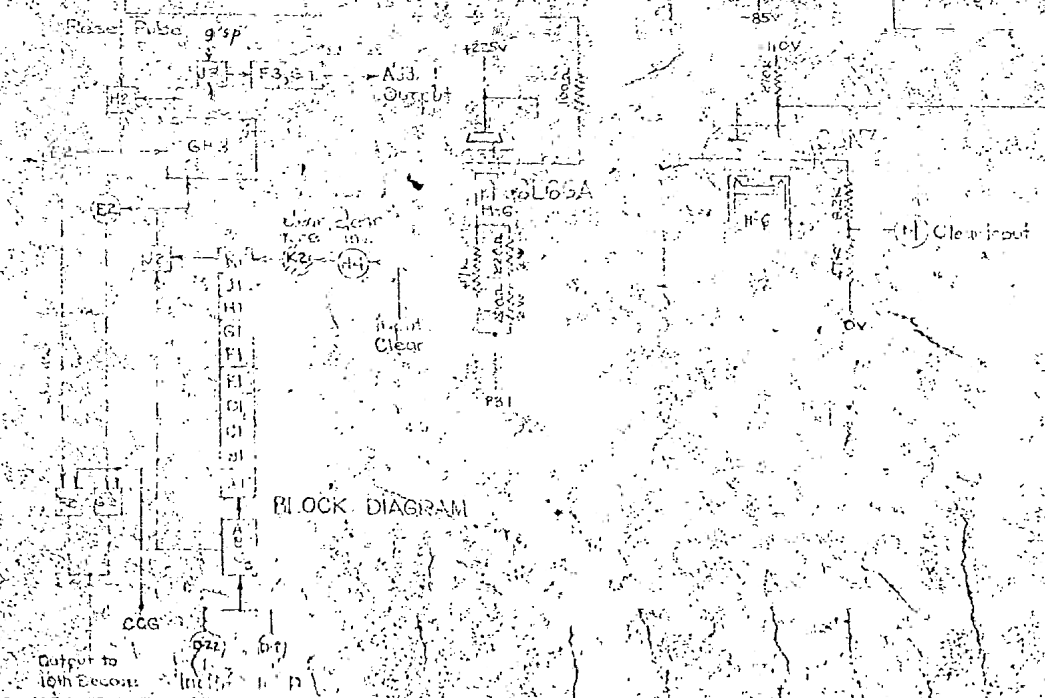


Socket PB Jones S-12AB	Socket PK Jones S-12AL	By-pass Condensers
1 Output Add	1 +225v	2.0 μf
2	2 +150v	0.5 μf
3	3 +110v	1 x 0.1 μf
4 3 Pulses Add	4 +75v	
5 Carry Gate	5 +50v	2 x 0.1 μf
6 Reset Pulse	6 +25v	2 x 0.1 μf
7 Carry over to 10th Dec.	7 0	
8 Decade Input	8 -10v	2 x 0.1 μf
9	9 -85	
10 0 Pulses	10 -130	2 x 0.1 μf
11	11 0	
12	12 110v	

Verticals on Jones Terminal Strip for 6SN7 from bottom

Pin	1st Bus	2nd Bus	3rd Bus
1	+20v	0v	0p
2	+75v	+50v	
3	+225v	+75v	
4	+75v	+20v	
5	+110v	-10v	Battery



ENIAC SECTION - COMPUTING LABORATORY  
ELEVENTH - HSM DECADE

7-1-49  
Drawn by S110  
Approved: **PX-4141**