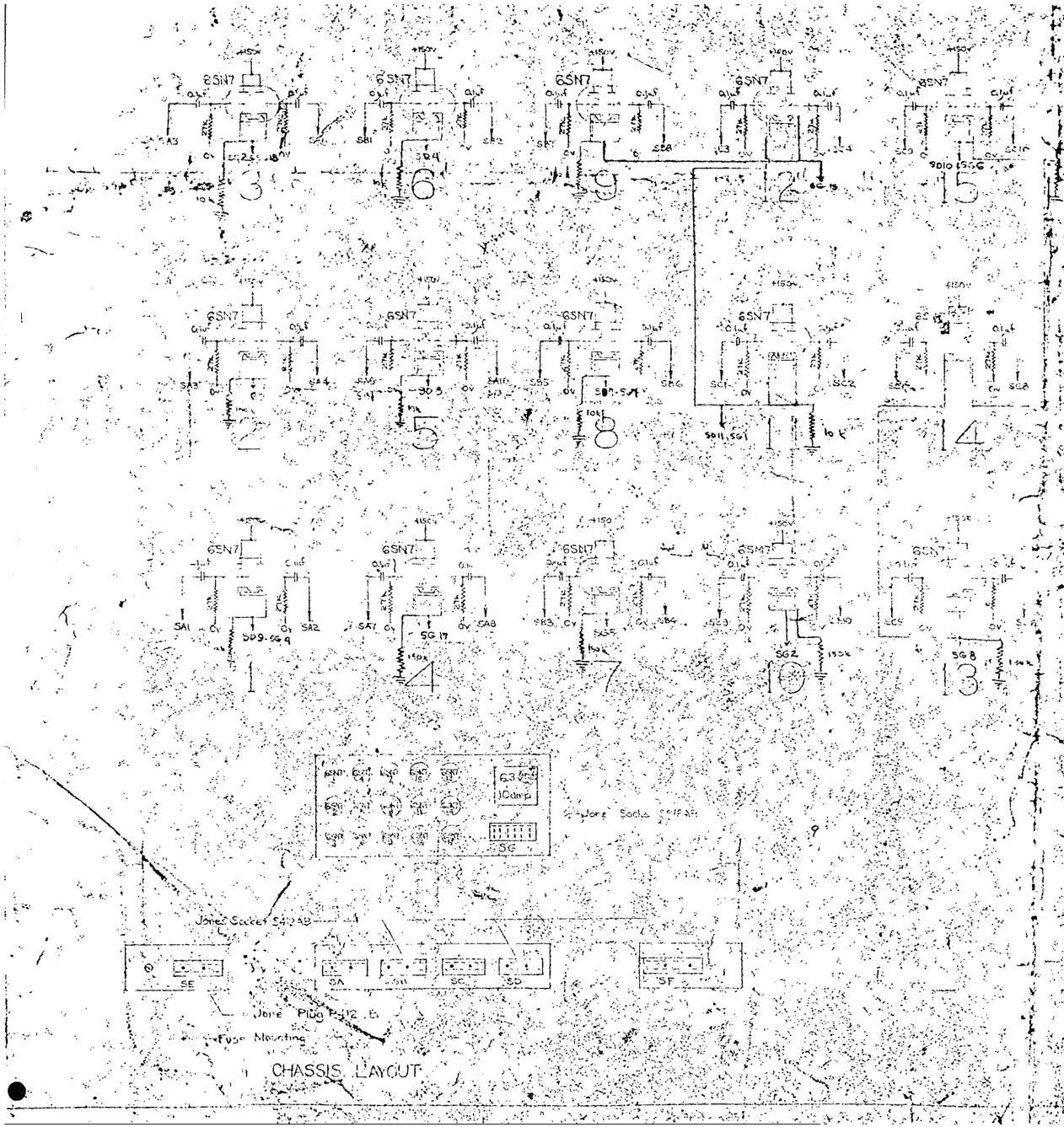

CHASSIS LAYOUT

SOCKET CONNECTIONS

Pin No.	Socket SA	Socket SB	Socket SC	Socket SD	Socket SE	Socket SF	Socket SG
1	Tube 3a	Tube 5a	Tube 10a	SG 16			SD 11 Tube 11
2	Tube 3b	Tube 5b	Tube 10b	Socket on Tube 8		6.3VAC	Tube 10
3	Tube 2a	Tube 6a	Tube 11a	SG 6	+225v	+225v	9.6VAC
4	Tube 2b	Tube 6b	Tube 11b	Tube 6	+150v	+150v	SD9 on Tube 7
5	Tube 3a	Tube 7a	Tube 12B	Socket on	+110v	+110v	Tube 7
6	Tube 3b	Tube 7b	Tube 13A	SG 12	+75v	+75v	SD10 on Tube 14 & 15
7	Tube 4a	Tube 8a	Tube 13B	Socket on Tube 8	+50v	+50v	SD 7
8	Tube 4b	Tube 8b	Tube 14A	Tube 5		+20v	Tubes 14 & 15
9	SG 11	Tube 9a	Tube 14B	Socket on Tube 5	4v	-4v	Tube 2
10	SG 11	Tube 9b	Tube 15A	SG 6 on Tube 19	-95v	-85v	SD5
11	BLANK	Tube 12A	Tube 15B	SG 1 Tube 18	-150v	-130v	SA-10
12	Ground	Ground	Ground	Ground	Ground	Ground	SD6
13							SD3
14							SA 3
15							Tube 12
16							SD1
17							Tube 4
18							SD2

ENIAC SECTION ~ COMPUTING LABORATORY

Drawn 7-[illegible]-49 SMG
Approved

PX4-145