

SAB

1	SAK Input
2	Clear Output
3	
4	-115 V
5	-130 V
6	40 V
7	+20 V
8	+50 V
9	+75 V
10	+110 V
11	+150 V
12	+250 V

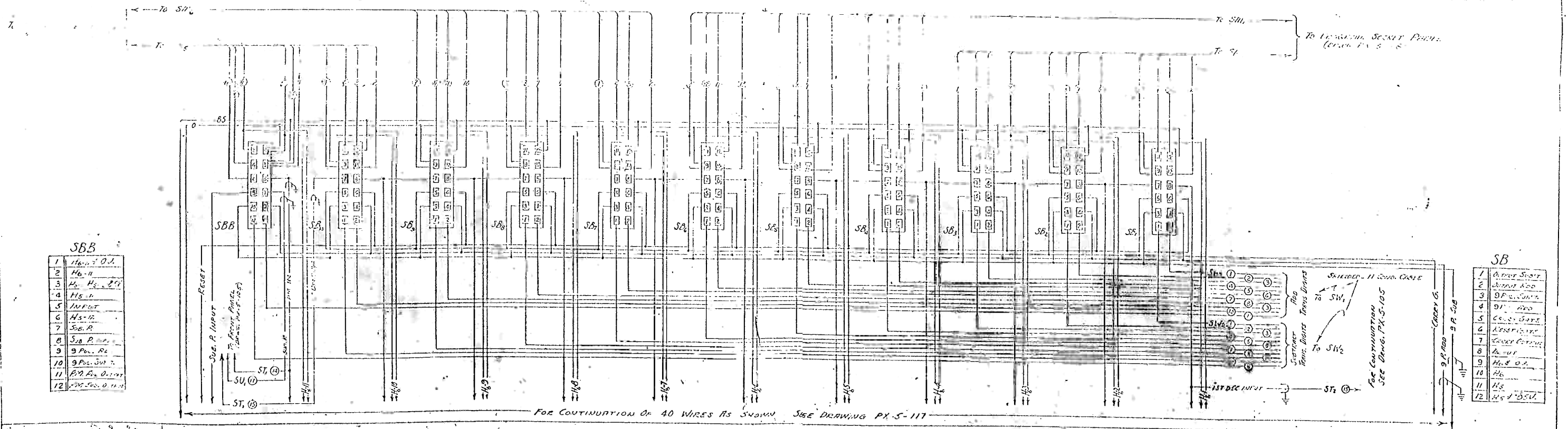
SA

1	Clear 5
2	Clear 6
3	Clear 7
4	Clear 8
5	-130 V
6	-40 V
7	+20 V
8	+50 V
9	+75 V
10	+110 V
11	+150 V
12	+250 V

To GATE CIRCUIT (1, 2, 3, 4)

Clear 5 Clear 6 Clear 7

For Continuation of Drawing PX-5-117



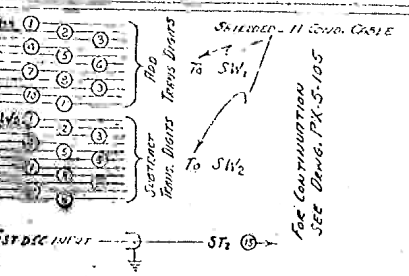
SBB

1	H6-1 O.I.
2	H6-11
3	H6-12
4	H5-1
5	INPUT
6	H5-12
7	SUB-R
8	SUB-R
9	Pos. Re
10	Pos. Sub 2
11	Pos. Sub 1
12	Pos. Sub 0

SB

1	Clear 5
2	Clear 6
3	Clear 7
4	Clear 8
5	Clear 9
6	Clear 10
7	Clear 11
8	Clear 12
9	H6-1 O.I.
10	H6-11
11	H6-12
12	H5-1

FOR CONTINUATION OF 40 WIRES AS SHOWN SEE DRAWING PX-5-117



MOORE SCHOOL OF ELECTRIC AND ELECTRONIC ENGINEERING
 U.S. DEPARTMENT OF THE ARMY
 Accumulator's Decodes Socket Panel
 MATERIAL FINISHED SCALE
 Drawn by: J. Cummings
 Checked by: H. G. [Signature]
 Approved by: [Signature]
 Date: April 4, 1944

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