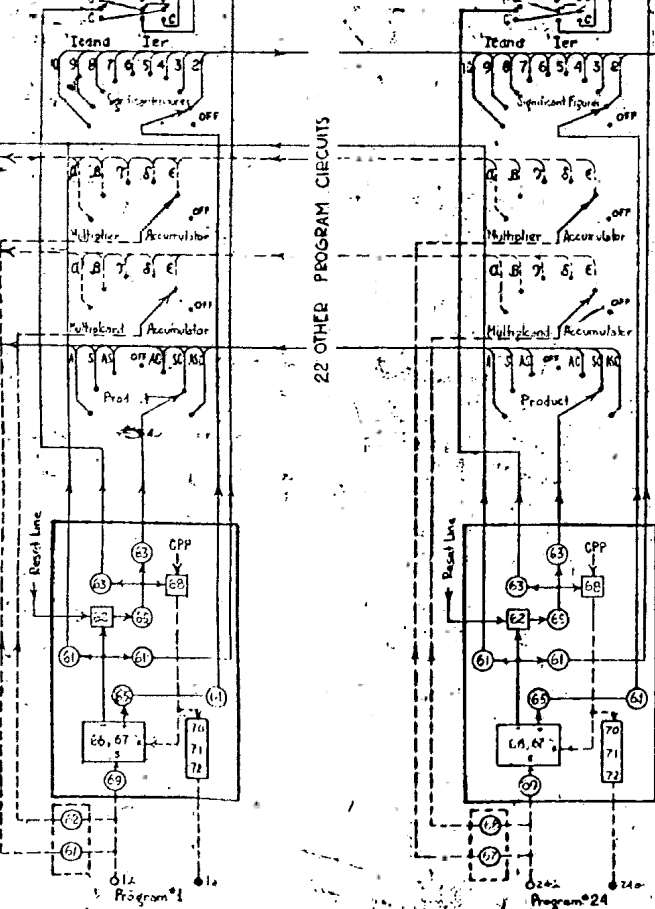


22 OTHER PROGRAM CIRCUITS



CODE

Gate tube	Flip-flop	Cited line - leads going both ways	CCG - Carry-Over Gate
Buffer or inverter	Receiver plug-in unit	Input terminal	CPP - Central Program Pulse
Pulse divider	Gate line	Input digit plug	ICG - Initial Clear Gate
Transmitter	Pulse line	Output digit plug	Pulse
	Cited line	Ring Counter	

Letters and numbers in the tubes denote position on the socket. Shifted tubes are normally conducting, unshifted ones are not.

MOORE SCHOOL OF ELECTRICITY
UNIVERSITY OF PENNSYLVANIA

MULTIPLIER BLOCK DIAGRAM

Drawn by: J.H. Moore
8-7-45