

OUTPUT GATES & DRIVER CIRCUITS (DRWG. PX-7-1)

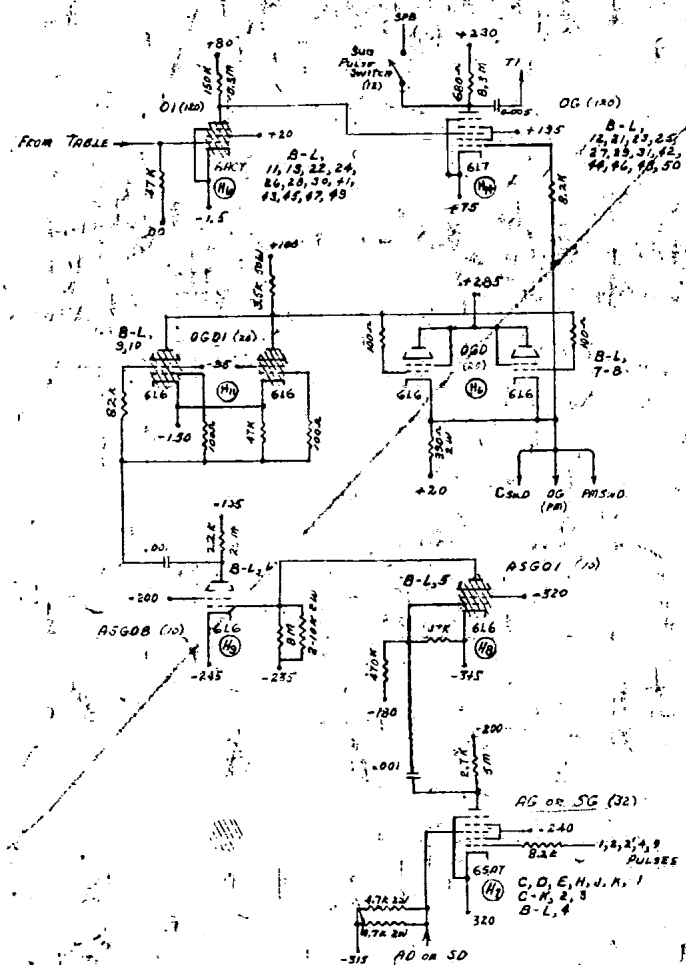
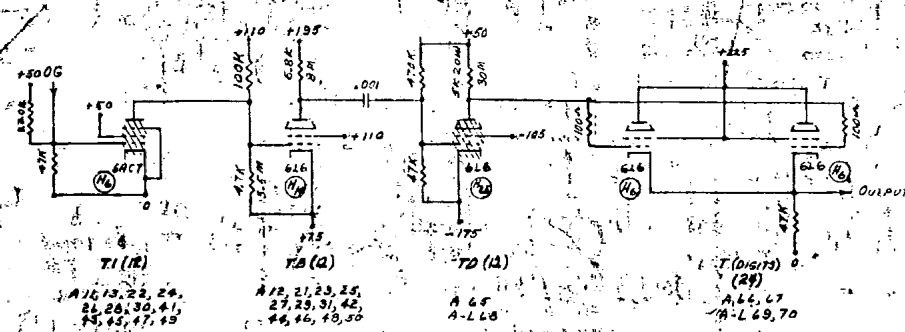
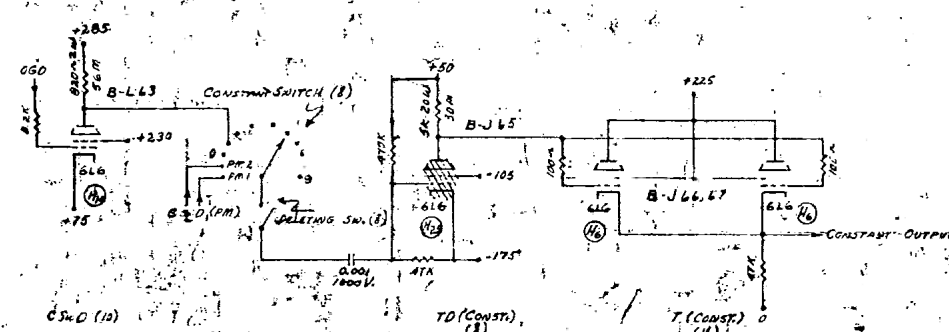


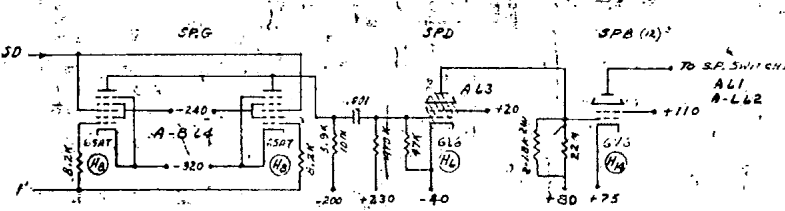
TABLE-CONTROLLED DIGIT OUTPUT CHANNEL (12) (DRWG. PX-7-150)



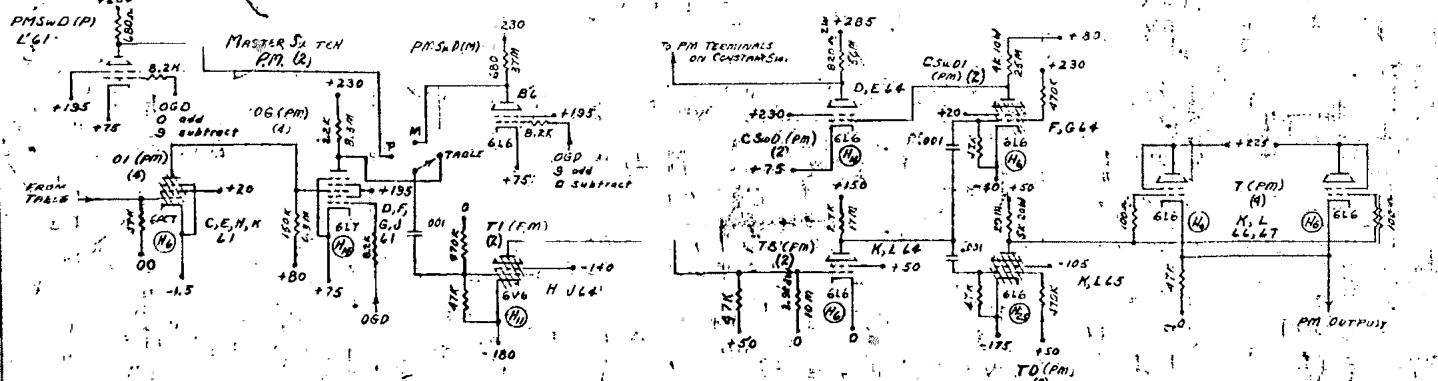
MASTER SWITCH & CONSTANT TRANSMITTER (2) (DRWG. PX 7-150)



SUBTRACT PULSE CHANNEL (DRWG. PX-7-1)



PM CHANNEL (2) (DRWG. PX-7-130)



D.C. VOLTS
 TUBE PART IN SYSTEM
 ADDED HEATER
 NUMBERS REVISED
 SCHEMATIC REVISION
 3
 4

MOORE SCHOOL OF ELECTRICAL ENGINEERING
 UNIVERSITY OF PENNSYLVANIA
 F.T. CROSS SECTION - PANEL 2
 Drawn by: MSM
 Checked by: RFS
 Approved by: RFS
 Date: 13 Nov 44
 Date: 24 Jan 45
 PX-7-115