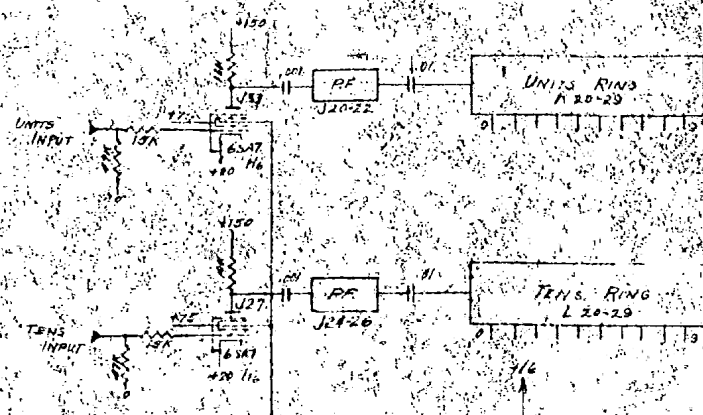
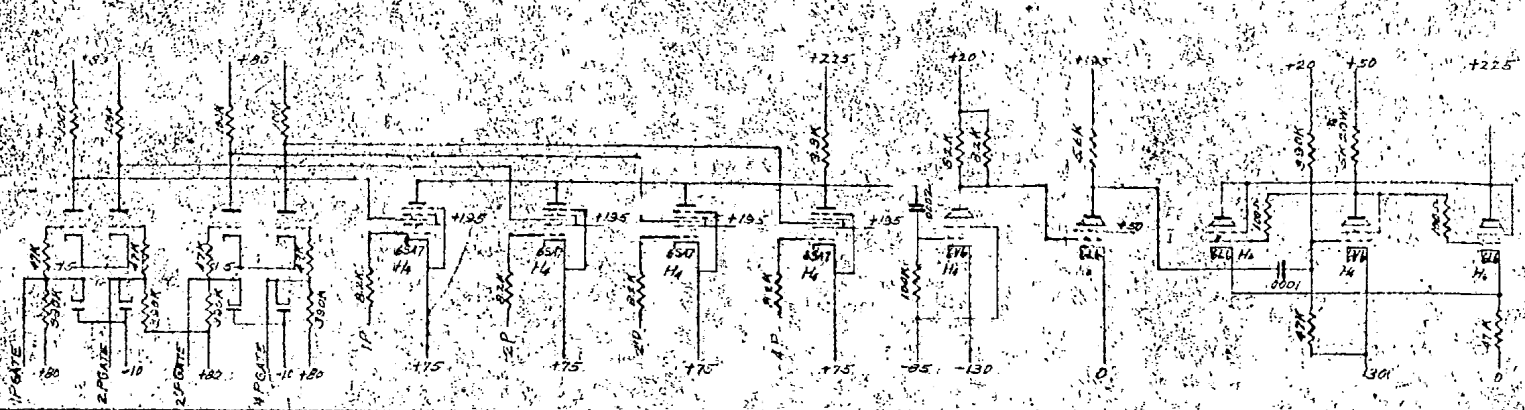


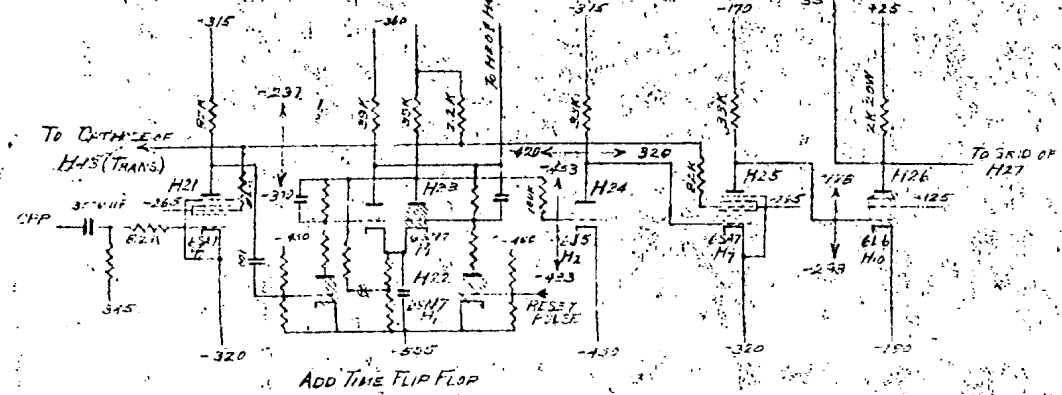
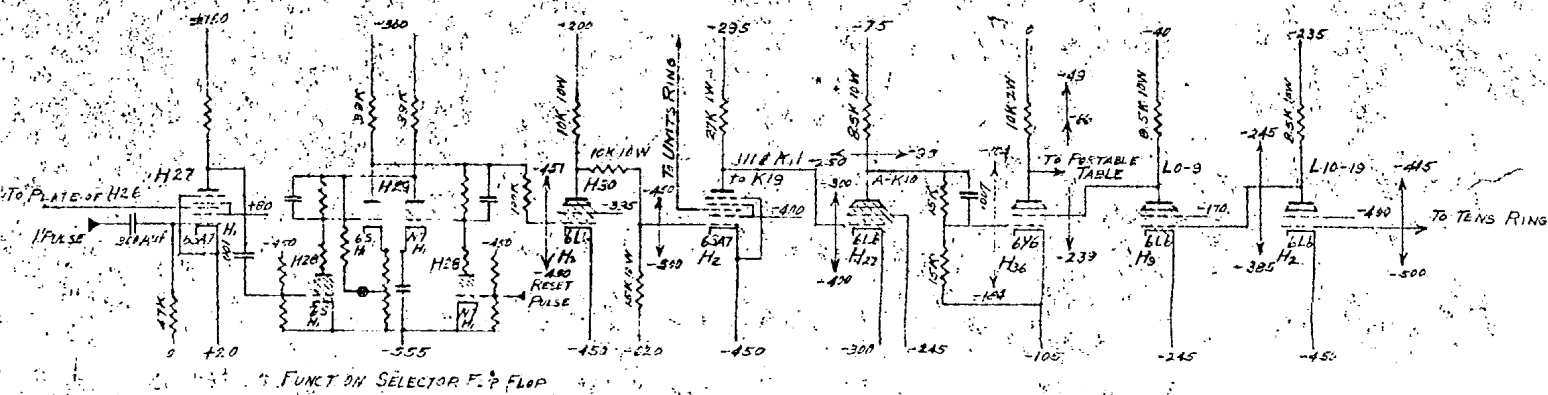
ADDRESS INPUT & CONTROL CIRCUITS (PX-7A-124, 121)



OUTPUT GATES & TRANSMITTER (PX-7A-127)



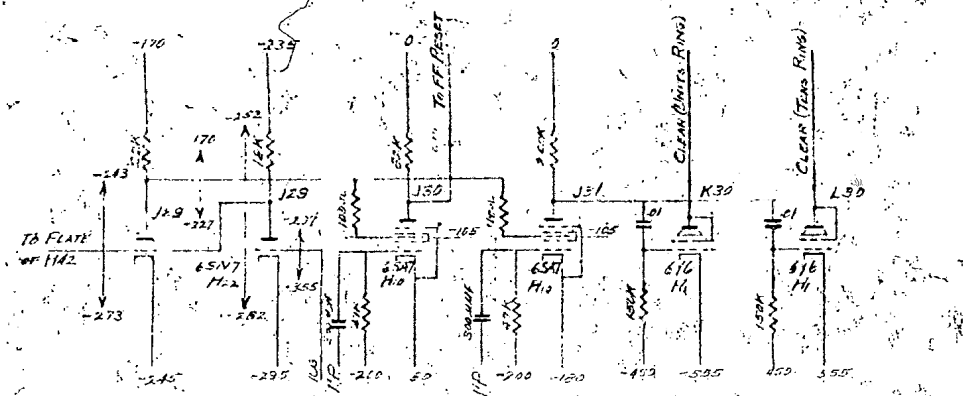
FUNCTION SELECTOR & ASSOCIATED CIRCUITS (PX-7A-125, PX-7A-120 & PX-7A-122)



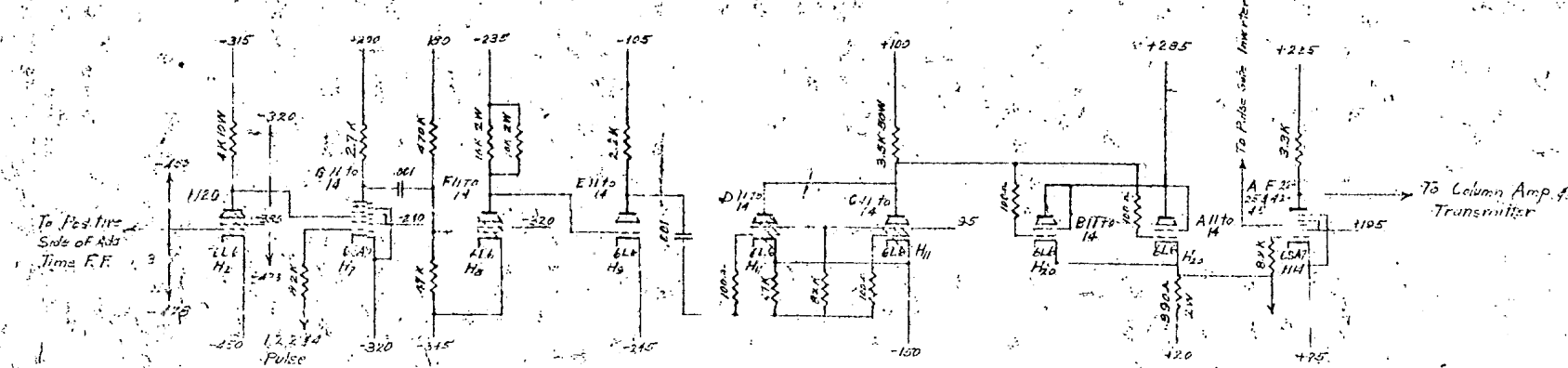
ADD TIME FLIP FLOP

FUNCTION SELECTOR F.F. FLOP

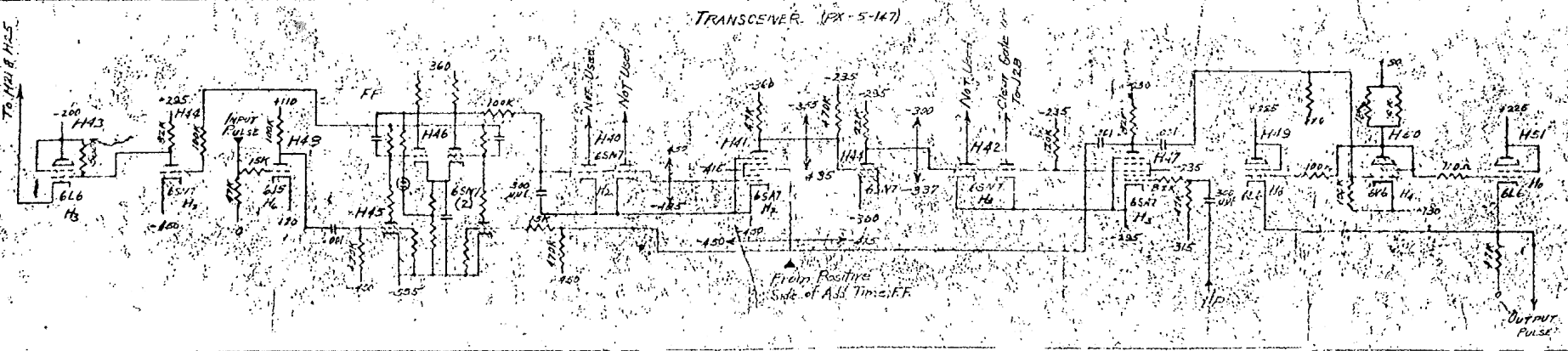
CLEAR CIRCUITS (PX-7A-121)



PULSE DRIVER & COLUMN OUTPUT GATES (PX-7A-127 & PX-7A-122)



TRANSMITTER (PX-5-147)



COMPUTER CENTER BRANCH BRL	
ARLINGTON HALL STATION	
FEDERAL BUREAU OF SURVEILLANCE	
Drawn by:	PX-7A-117
App. 20.101	