



PT	
1	STEPPER INPUT BUF.
2	STEPPER DIRECT IN. BUF.
3	+2.0
4	CYCL. GATE INV. IN.
5	+2.5
6	CPP
7	+1.10
8	PULSE DRIVER OUTPUT
9	PROGRAM INPUT
10	+2.25
11	+50
12	+75

PU	
13	+150
14	+4.60
15	-75
16	/

PV	
1	H <sub>5</sub>
2	H <sub>10</sub>
3	0, H <sub>6</sub>
4	H <sub>6</sub>
5	+2.90
6	+2.85, H <sub>8</sub>
7	+2.20, H <sub>7</sub>
8	H <sub>7</sub>
9	-130
10	-85, H <sub>5</sub>
11	+2.60
12	+370

NOTE:  
 1. ALL RESISTORS ARE 1/2 WATT, UNLESS OTHERWISE S + ED.  
 2. \* FOR RESISTORS & CONDENSERS 50 M KED, SEE CHART ON DRAWING PX-5-133.

REVISED 8/2/45  
 Please write on rest gate vs +10, +150 added on PU  
 R. J. Allen 2-7-45  
 VOLTAGES REVISED  
 R. J. Allen 5-15-45  
 GRID CONNECTIONS  
 TUBE 65 REVISED  
 R. J. Allen 5-15-45

Master Programme: Program 111-10

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