



S T		S W	
1	STEP IN. BUS	1	PULSE INPUT
2	STEP DIR. IN.	2	+20
3	+20	3	OUT PUT #3
4	CYC. GATE IN/IN	4	TRANS. GATE #2
5	+25	5	OUT PUT #2
6	C.P.P.	6	TRANS. GATE #0
7	+110	7	TRANS. GATE #1
8	PULSE OUTPUT	8	TRANS. GATE #2
9	PROG. INPUT	9	+2.25
10	+20	10	+20
11	+75	11	+50
12		12	+75

S B C		S B A	
1	50 OUTPUT #6	1	TRANS. GATE #1
2		2	
3		3	
4		4	
5		5	
6		6	
7	PULSE OUTPUT	7	
8		8	
9		9	
10		10	
11		11	
12	GROUND	12	GROUND

S U		S V		S X	
13	+150	1	H5	1	+255 H10
14	+240	2	H10	2	H10
15	-75	3	O.H.6	3	H6
16		4	H6	4	-130 H4
17		5	+200	5	
18		6	+205 H10	6	+230
19		7	+220 H17	7	H4P
20		8	H17	8	
21		9	-130	9	H4A
22		10	-85 H5	10	O.H.4, H4B
		11	+260	11	
		12	+370	12	

REVISIONS  
 REVISED BY: [Signature]  
 DATE: 2/20/45  
 CHANGED TO: [Signature]  
 2204-370 REPERCUSSION  
 2  
 VOLTAGE TERMINALS  
 REVISED BY: [Signature]  
 DATE: 2/20/45  
 CHANGED TO: [Signature]  
 2204-370 REPERCUSSION  
 4  
 SX3  
 2/20/45

MOORE SCHOOL OF ELECTRICAL ENGINEERING  
 UNIVERSITY OF PENNSYLVANIA  
 M.P. PROGRAM IN TRANSMITTER SOCIETY  
 MATERIAL FINISH  
 Drawn by: C.J.M.C. Nov. 16, 1944  
 Checked by: [Signature] 11/1/44  
 Approved by: [Signature] 11/1/44  
 PX-8 10