



THE MASTER PROGRAMMER PANEL #2 WAS MODIFIED TO BE USED AS A TEN DIGIT REGISTER. A BINARY COUNTER WAS ADDED TO INDICATE THE SIGN AND AN ADDRESS GENERATOR WAS BUILT FROM STANDARD HIGH SPEED FUNCTION TABLE GATE AND TRANSMITTER UNITS TO PROGRAM THE MEMORY OR FUNCTION TABLE TO TRANSMIT A POSITION.

THE DIAGRAMS ASSOCIATED WITH THIS MODIFICATION ARE:

- PX-8-305 M.P. PANEL #2 MODIFICATION - LOGICAL
- PX-8-129 P.M. INDICATOR FOR M.P. REGISTER
- PX-8-130 CROSS SECTION FOR P.M. INDICATOR OF THE M.P.
- PX-8-121 ADAPTER CHASSIS FOR M.P. REGISTER ADDRESS GENERATOR - WIRING

SEE PX-7A-127

MINN. CA. 467 CIV 138  
 PLAINTIFF'S TRIAL EXHIBIT  
 No 8438  
 015299

SYM	DESCRIPTION	DATE	APPROVAL

PHYSICAL PROPERTIES		UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES		ORIGINAL DATE OF DRAWING 11/12/53	
YP		TOLERANCES ON	DECIMALS	DRAFTSMAN <i>ALD</i>	CHECKER
TS		ANGLES	FRACTIONS	TRACER	CHECKER
EL 2		MATERIAL		ENGINEER	ENGINEER
RA				SUBMITTED	
BH		NEXT ASSY	USED ON	ORD CORPS	
RH		APPLICATION		APPROVED BY ORDER OF THE CHIEF OF ORDNANCE	
		DO NOT	APPLY PART NO.	ORD CORPS	
		DQ	AS SPECIFIED	SCALE	UNIT WT

M.P. PANEL #2  
 MODIFICATION  
 LOGICAL

ORDNANCE CORPS  
 DEPT OF THE ARMY

DWG SIZE PX-8-305  
 SHEET OF