

TABLE 10-1  
PROPERTIES OF MASTER PROGRAMMER INPUTS

t = addition time when terminal is pulsed unless otherwise noted.  
 s = stage of stepper counter before a pulse is received.  
 $d_s$  = number set up on decade switches associated with stage s of stepper counter.  
 c = number set up on stepper clear switch

INPUT TERMINAL	PULSE INPUT	EFFECT OF RECEPTION OF A PULSE	ADDITION TIME EFFECT OCCURS
Stepper Input	Program pulse at end of add. time t or PM pulses during add. time t+1.	1. Output pulse is transmitted through output terminal corresponding to stage s of stepper counter. 2. Decade counters cycle 1 stage in units place 3. If input cycles decade counters to $d_s$ a. decade counters clear to zero b. stepper counter cycles to $(s+1) \text{ mod } c$	t + 1 t + 1 t + 2 t + 2
Stepper Input A or F with decades dissociated	Program pulse at end of add. time t or PM pulses during add. time t+1.	1. Output pulse is transmitted through output terminal corresponding to stage s of stepper counter. 2. (No decade counters) 3. (No decade counters)	t + 1
Stepper Direct Input	Digit or program pulse	1. No output pulse is transmitted 2. Decade counters do not cycle 3. Stepper counter cycles 1 stage for each pulse received.	Immediately
Stepper Direct Input A or F with decades dissociated	Digit or program pulse	1. No output pulse is transmitted 2. (No decade counters) 3. Stepper counter cycles 1 stage for each pulse received.	Immediately
Stepper Input and Stepper Direct Input	Program pulse at end of add. time t or p digit pulses during add. time t+1.	1. Output pulse is transmitted through output terminal associated with stage $(s+p)$ of stepper counter. 2. Decade counters cycle 1 stage in units place. 3. Stepper counter cycles 1 stage for each pulse received at stepper direct input terminal. 4. If decade counters are cycled to stage $d_{s+p}$ a. decade counters clear to zero b. stepper counter cycles to $(s+p+1) \text{ mod } c$ .	t + 1 t + 1 Immediately t + 2 t + 2
Stepper Input and Stepper Direct input A or F with no decades	Program pulse at end of add. time t or p digit pulses during add. time t+1.	1. Output pulse is transmitted through stage $(s+p) \text{ mod } c$ . 2. (No decade counter) 3. Stepper counter cycles 1 stage for each pulse received.	t + 1 Immediately Immediately
Decade Direct Input	Program pulse	1. No output pulse is transmitted 2. Decade counter cycles one stage 3. If decade counters are cycled to stage $d_s$ a. decade counters clear to zero b. stepper counter cycles to stage $(s+1) \text{ mod } c$	Immediately t + 1 t + 1
Stepper Clear Direct Input	Program pulse or digit pulses	1. Stepper counter clears to stage 1	Immediately