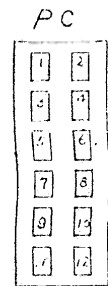
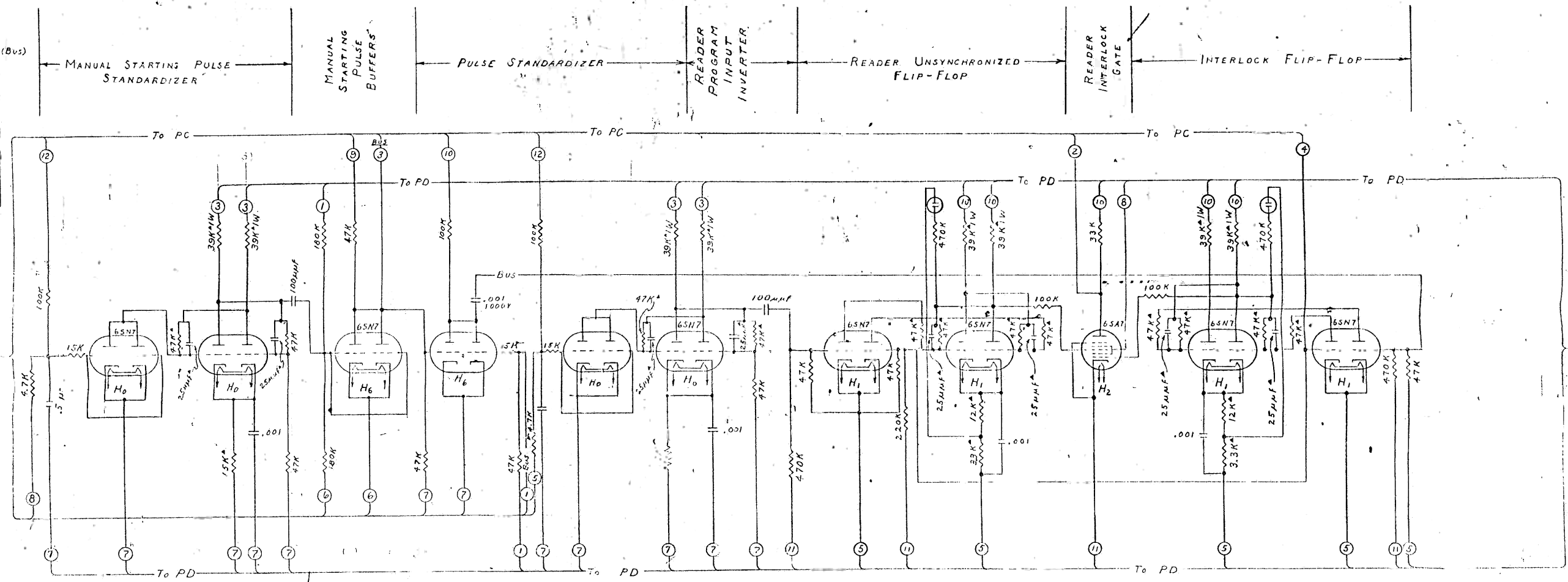
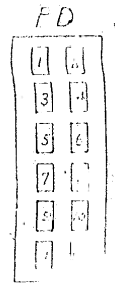


| PC | |
|----|-------------------------|
| 1 | READER INTERINPUT (BUS) |
| 2 | INTERLOCK OUTPUT |
| 3 | MAN. PROG. OUTPUT |
| 4 | RESET & INT. CLEAR |
| 5 | READ. FINISH INPUT |
| 6 | -40 (WHITE) |
| 7 | +20 (WHITE) |
| 8 | READ. MAN. PROG. IN. |
| 9 | +75 (BLACK) |
| 10 | +110 (BLACK) |
| 11 | |
| 12 | +110 |



| PD | |
|----|-----------|
| 1 | 0 & H6 |
| 2 | H6 |
| 3 | +285 |
| 4 | |
| 5 | -555 & H1 |
| 6 | H1 |
| 7 | H6 & 75 |
| 8 | -415 |
| 9 | H6 |
| 10 | -360 |
| 11 | -450 & H2 |
| 12 | H2 |



NOTE:
 1. ALL RESISTORS ARE 1/2 WATT, UNLESS OTHERWISE STATED.
 2. * FOR RESISTORS AND CONDENSERS SO MARKED, SEE CHART ON DRWG. PX-5-133.

61 62 63 64 65 66 67 68 69 70 71

| REVISIONS | |
|--|---|
| (1) 6555 (2) 6117 | 1 |
| 7.15. 5/15/45 | 2 |
| READER PROGRAM INPUT INVERTER DRAWN IN CORRECT PHYSICAL LOCATION. WIRING TO FIRST 6 TUBES REVISED. | 3 |
| TUBE NUMBERS ADDED. 47K RESISTOR ADDED ON TUBE 66. | |
| 9-27-45 | |

MOORE SCHOOL OF ELECTRICAL UNIVERSITY OF PENNSYLVANIA

READER INTERLOCKING PLUG-IN

| | | |
|-----------------------|-------------|--------------|
| DESIGNED BY: CJM:c | CHECKED BY: | APPROVED BY: |
| AUG. 29, 1944 | | |

PX-9-1