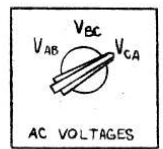
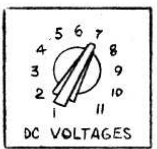
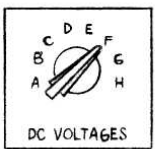
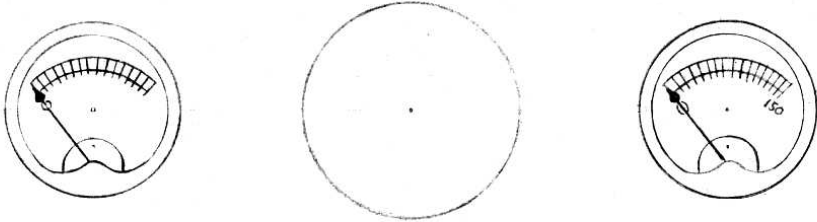


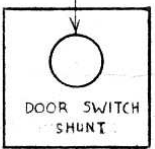
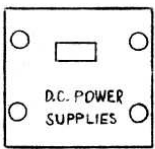
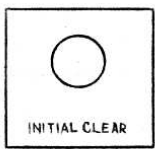
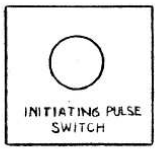
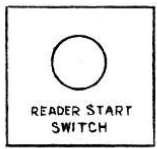
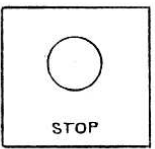
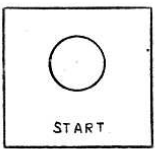
THE ENIAC

ELECTRONIC NUMERICAL INTEGRATOR AND COMPUTER

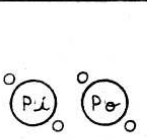
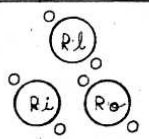
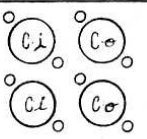
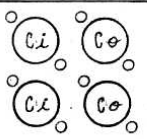
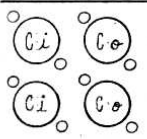
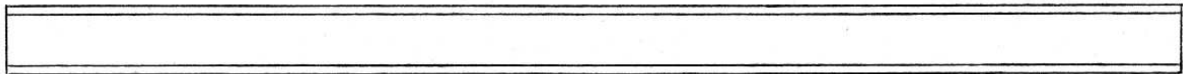
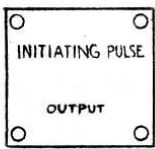
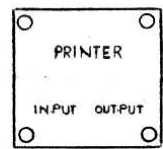
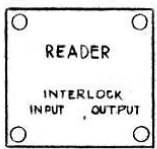
DEVELOPED, DESIGNED AND CONSTRUCTED
BY THE
MOORE SCHOOL OF ELECTRICAL ENGINEERING
OF THE
UNIVERSITY OF PENNSYLVANIA
1944



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A											
B											
C											
D											
E											
F											
G											
H											



Back Cover SWITCH SHUNT



Ci - pulse input terminal for selective clearing.
Co - pulse output terminal for selective clearing

Ri - pulse input terminal for reader interlock signal.

Rl - pulse input terminal for reader.
Ro - pulse output terminal for reader

INITIATING DEVICE
FRONT PANEL
PX-9-302 R