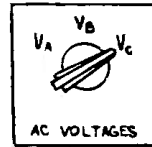
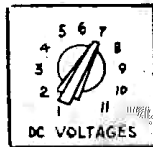
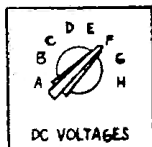
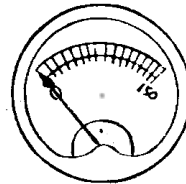
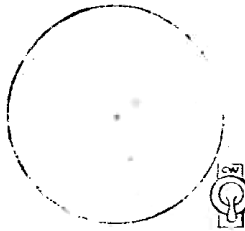
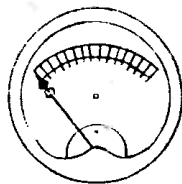


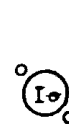
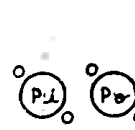
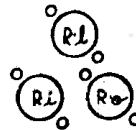
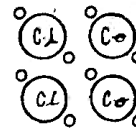
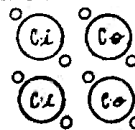
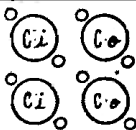
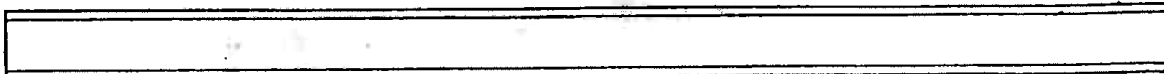
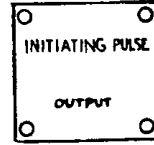
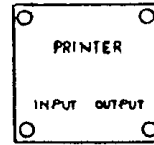
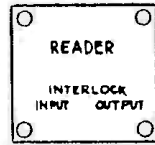
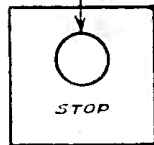
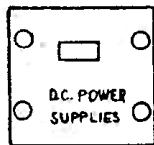
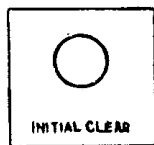
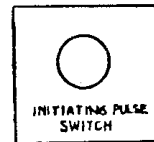
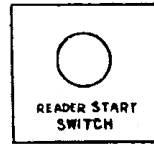
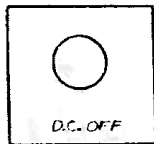
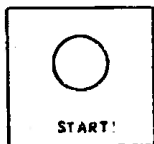
THE ENIAC

ELECTRONIC NUMERICAL INTEGRATOR AND COMPUTER

DEVELOPED, DESIGNED AND CONSTRUCTED
BY THE
MOORE SCHOOL OF ELECTRICAL ENGINEERING
OF THE
UNIVERSITY OF PENNSYLVANIA
1944



	1	2	3	4	5	6	7	8	9	10	11
A											
B											
C											
D											
E											
F											
G											
H											



Ci - pulse input terminal for selective clearing.
Co - pulse output terminal for selective clearing.

Ri - pulse input terminal for reader interlock signal.

Ro - pulse output terminal for reader.

INITIATING DEVICE
FRONT PANEL
PX-9:302 R